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### (54) Improvements relating to a video data receiver and method of displaying these data

(57) The invention relates to an improvement in receivers for receiving broadcast video data for generating a video display from said data and also to the method of displaying said data. The invention provides for a memory into which received data can be directed and said data may also at the same time or previously have been used to generate a video display on a display screen. The data in the memory can be called up to generate a video display thereafter and the speed of the display

may be selected so as to provide, for example, a slow motion or freeze frame display. The facility allows the user to select to regenerate a display which they may already have seen at the initial video display and further to generate the display in a slow motion sequence. The memory can be updated with newly received data. In addition, or alternatively, the memory may be provided to receive packets of data which are selected for storage and said packets of data in conjunction to provide a data sequence which can be selected to be viewed.

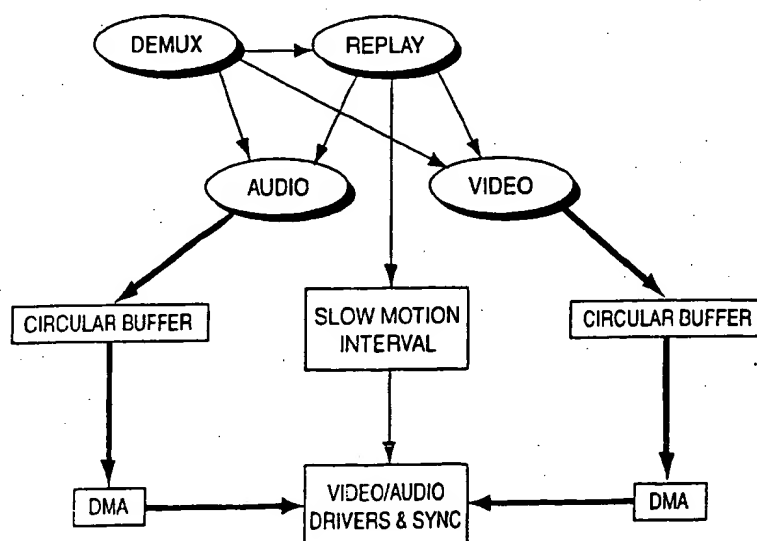


FIG 3

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## Description

[0001] The subject matter of this invention relates to improvements for receivers of the type used to receive transmitted broadcast video signals and particularly, although not necessarily exclusively, the digital signal receiver. A digital receiver allows the transmitted data which is in an encoded format to improve efficiency and reduce code length, to be received, decoded and displayed on screen for viewing.

[0002] A known problem with digital data receivers is that while they decode and display broadcast information efficiently they are at present only capable of acting as "real time" processing devices and do not hold any significant historical information. This means that the viewer of the television or other means connected to the receiver is denied the opportunity to repeat the last quantity of video data to be displayed in normal replay or slow motion mode. This is however a feature which has been found to be desirable as the viewer may wish to review some material which has previously been displayed and can be used in many different functions such as, for example, when viewing sports events to look at a goal or particularly skilful piece of play.

[0003] The aim of the present invention is to allow the ability for a receiver to be provided with replay and/or slow motion functions for video content which is received and displayed on a television set or other display medium.

[0004] In a first aspect of the invention there is provided a receiver for the reception of transmitted data and generation of a display of the video data on a display screen connected therewith, said receiver including a physical memory in which video data is stored and from which, for a time period following display, said video data can be selectively retrieved to create a video display on the display screen and characterised in that said memory includes a first portion of memory which receives the most recently transmitted data received by the receiver at each instant, and a remainder portion in which is stored, in sequence, packets of data which have previously been received and displayed and have been transferred from said first portion over time.

[0005] In a preferred embodiment of the apparatus there is provided a transmitted data receiver for the reception of data and generation of a display of the video data on a display screen and wherein said receiver includes a physical memory, in which video data is stored, which video data can be selectively used to create a video display on the display screen and the memory includes a first portion which acts as a moving buffer memory portion which moves and is updated with the passage of time and into which the most recently displayed data packets are stored. Over time the first portion is updated with more recent data packets and the data packets previously held in the first portion move into the second portion of the memory which stores the data packets in time order for a period of time, the length of

which can be determined at least partially by the size of the memory. Typically a circular memory construction is used and, as the first portion "moves" around the memory, and is updated and receives the most recently displayed data, so the oldest data held in the second portion is overwritten and this process continues as data continues to be processed and displayed by the receiver. Thus in a preferred embodiment, at any one time, the memory, in accordance with the invention, holds data which has most recently been processed and displayed and further data in time order therefrom up to a certain historical time limit and the data held in that memory can be retrieved for display upon command by the user of the apparatus and in one of plurality of user selectable formats.

[0006] In one embodiment the video data stored in the memory has been used to generate a video display.

[0007] Thus the memory receives data from the data transport stream from the broadcaster and the same is used to generate the video display and at the same time is stored in the physical memory so that at any instance a predesignated quantity of the most recently received data is readily available in the memory.

[0008] Preferably the memory is a circular buffer memory in operation which means that the content of the memory at any one time is successively overwritten as new packets of video data are routed to and through the memory to replace the oldest data in the memory and so on.

[0009] Preferably the capacity of the memory is set so as to be sufficient to allow the storage of meaningful quantities of data so as to facilitate the storage of a displayed video content or a series of packets of data so that the video packets are accessible for a predetermined period of time following display to allow the same to be replayed or for the same to be used in slow motion. Typically the length of the video packets which can be viewed after initial display is directly proportional to the size of the buffer memory provided and the capacity is such as to allow a video display of a meaningful length to be displayed in slow motion or replay mode.

[0010] In a further feature of the invention, the display interrupt at the video driver layer can be manipulated in respect of the contents of the buffer memory so that the same can be displayed at variable speeds to achieve the effect of variable slow motion.

[0011] In a further aspect of the invention there is provided a method of receiving and displaying transmitted data, said method comprising the steps of:

encoding a stream of data into digital encoded data; transmitting the encoded data for reception by at least one data receiver; decoding the received data into a form for the generation of a video display on a display screen connected to the data receiver and characterised in that the data receiver includes or is connected to a memory in which the received data can be stored for se-

lected retrieval within a time period after the initial display to create a video display, and wherein said memory includes a first portion into which the most recently received data is placed, and a remainder portion into which data from the first portion passes.

[0012] In one embodiment the received data is simultaneously decoded to create the video display and saved in the memory for selective use in generating a display.

[0013] In one embodiment the memory contains packets of received data with the oldest packets of data overwritten by most recently received packets of data over time.

[0014] In an alternative embodiment packets of data can be selectively stored in a memory to generate a series of packets of data for selected display, such as for example, a series of packets of data representing the highlights of a broadcast sporting event.

[0015] In one embodiment the speed of the display of data from the memory can be selected, and may be, for example, selected to provide a slow motion display of the data, or even a freeze frame effect.

[0016] In a yet further aspect of the invention there is provided a receiver for the reception of transmitted data and generation of a display of the video data on a display screen connected therewith, said receiver including a physical memory in which video data is stored and from which, for a time period following display, said video data can be selectively retrieved to create a video display on the display screen and characterised in that said memory includes a first portion defined as the portion of the memory defined between the producer and consumer pointers which receives the most recently transmitted data received by the receiver at each instant, and a remainder portion of the memory and in which is stored, in sequence, packets of data which have previously been received and displayed and which are transferred from said first portion as producer and consumer pointers move around the circular memory configuration.

[0017] The invention as herein described provides novel features in that it can provide a freeze frame functionality, can allow the replay of previously displayed video clips and can allow the replay of the same as many times as required. In other functions, the invention also provides for slow motion functionality and allows for the variation in slow motion speed as required.

[0018] Specific embodiments of the invention will now be described with reference to the accompanying drawings, wherein:-

Figure 1 illustrates the basic interaction of the main processes responsible for TS input, de-scrambling, de-multiplexing/routing of the TS content and the video replay task;

Figure 2 illustrates the operation of the memory in the form of a buffer in one embodiment; and

Figure 3 illustrates in schematic form the replay of the stored video data.

[0019] To allow the receiver with memory storage to be operated the following steps are followed with reference to Figure 1. Tasks are initialised at start-up and instantiated to run concurrently: The TS-IN process compares the Packet ID (PID) of the incoming Transport Packet with the PID Action Table held in the memory of the receiver to distinguish between packets that should be routed to the memory and the packets of data which can be rejected. The TS-IN process initially searches for PID 0 which contains the Program Association Table (PAT). The DVBC process determines whether or not the data packet needs unscrambling and, if so, and a valid key exists, the packets are decrypted and repacked before being passed on to the DEMUX process. The DEMUX process establishes the type of payload by examining and extracting the relevant information from the PID table and routing it to the relevant process. For example, if the information is video, it is routed to the VIDEO process and so on. VIDEO, AUDIO and other consumer processes provide simple interfaces to the device drivers. The REPLAY process interacts with the DEMUX/Routing process and the Video process. The VIDEO process typically can not receive video packets from DEMUX and REPLAY processes simultaneously. Circular buffer memories are used for inter-process communication.

[0020] The interaction between the DEMUX routing process (producer) and the VIDEO process (consumer) utilises a short buffer of few data packets. This is to say that as soon as these few packets of video data are available, they are routed to the video process. This, in turn, interacts with the video driver. Thus, the producer pointer is constantly moving round the circular buffer and is followed closely by the consumer pointer as illustrated in Figure 2 to define a first portion of the memory in accordance with the invention. The dotted line 2 consists of previously decoded and displayed video packets and is referred to as the second or remainder portion of the memory in accordance with the invention also referred to as the history loop. When the receiver or set top box (STB) is instructed to operate the video replay mode such as by the reception of a signal from a remote control, the REPLAY process first interrupts the DEMUX process and halts the subsequent routing of the video data packets. The REPLAY process then takes over the task of the DEMUX/Routing process and channels the video packets in residence in the history loop (in the memory) to the VIDEO process.

[0021] The implementation of video replay is achieved by accessing the video packets in the history loop and the packets are subsequently interfaced to the video driver. The length of the history loop is dependent on the physical size of the memory in the receiver in accordance with the invention.

[0022] The implementation of slow motion replay is

somewhat different in that, while video replay may be accomplished at process level, the slow motion functionality is implemented at driver level. This is because packets depicted in the circular buffer memory have no timing relationship with adjacent packets. Thus a simple delay mechanism between packet transfer to video driver would not result in the desired slow motion effect. As shown in Figure 3, the REPLAY task sets the slow motion interval and this is used as a parameter in the driver interrupt handling routine to adjust the number of times a picture field/frame is displayed.

[0023] There are two approaches to achieve the desired effect in slow motion depending on whether the source picture encoding is field-based or frame-based. In the case of frame based encoded video data, both display fields of the picture are at the same temporal reference. In this case the slow motion is achieved by repeatedly displaying both fields of the decoded picture to obtain the desired degree of slow motion. In the case of field-based encoded video data the two fields belonging to the same picture are at different time instances with the second field occurring after the first. In this scenario the slow motion requires the repetition of each field separately. Two known techniques; vertical intra-field interpolation or horizontal inter-field interpolation, have been implemented to smooth out the effect of artefacts.

[0024] Another implication of slow motion replay is the number of frame (picture) buffers allocated for decoding and displaying a sequence. In a 4-frame buffer allocation, 2 frames are utilised for reference frames (I or P frames) and in the worst case scenario in a GOP sequence, whereby there is more than one B frame between two reference frames, one frame buffer is used for decoding the current B frame while the remaining frame buffer is used to display the previously decoded B frame. In a 3-frame implementation two reference frame buffers are used, leaving one buffer to share the decoding and displaying the B frames. This is achieved by overwriting the rows of the buffer already displayed with the decoded information for the next frame. Thus in a 3-frame buffer allocation, where the decoding and display of the video data is field-based, the information in the buffer for B frame is overwritten and potentially lost at every VSYNC. In this case the decoding must be halted during displaying the same B frame repeatedly to achieve the slow motion effect.

[0025] A specific example of the video replay and slow motion functions is now described for the purpose of illustration and in a non-limiting manner on the interpretation of the scope of the patent application and in the invention described herein.

[0026] In this specific example, the physical memory in the receiver is increased from 4 MBytes to 8 Mbytes of DRAM. This provides a memory heap size of 6 Mbytes which is capable of containing a link-list of 30,000 video packets in the history or second portion of the memory loop which equates to about six seconds of video replay in real time.

[0027] The video replay is therefore implemented using the following steps:

1. Interrupt and halt the DEMUX/Routing process
2. Assign the consumer pointer to the producer pointer (i.e. at the beginning of the history loop)
3. Advance the consumer pointer to the NEXT video packet
4. Route the video packet to the video process
5. DMA the history packets to the video driver
6. Decode and display the video packets in the history loop
7. Repeat from No. 3 until the consumer pointer completes the history loop

[0028] The video replay in slow motion can be implemented using in this embodiment 3-frame buffer allocation for A GOP of I.B.P.B.P....B sequence with field based decoding of pictures. The slow motion is then implemented by repeating the display function for both fields of each frame in the GOP sequence.

[0029] The implementation of freeze frame can also be achieved by interrupting the DEMUX process to halt the Routing of video content to the driver. For improved clarity of image, the freeze frame avoids B frames and latches to the nearest reference frame (I or P frame).

[0030] These functionalities can be operated by a remote control and accessed by a freeze-frame button on the remote control to halt the DEMUX/Routing process. Once the receiver is in freeze-frame/replay mode, other buttons are used for replay and slow motion functionalities. Button no. 1 is used for normal replay and button nos. 2 to 9 are used for varying the speed of slow motion replay with button no. 9 being the slower replay (about 2.8 frames per second) select button. Pressing the freeze-frame button again re-installs DEMUX/routing tasks and the receiver switches to normal mode.

#### Claims

1. A receiver for the reception of transmitted data and generation of a display of the video data on a display screen connected therewith, said receiver including a physical memory in which video data is stored and from which, for a time period following display, said video data can be selectively retrieved to create a video display on the display screen and characterised in that said memory includes a first portion of memory which receives the most recently transmitted data received by the receiver at each instant, and a remainder portion in which is stored, in sequence, packets of data which have previously been received and displayed and which are transferred from said first portion over time.
2. A transmitted data receiver according to claim 1 characterised in that the input of data into the first

portion causes the movement of previously held data from the first portion into the remainder portion and, if the capacity of the remainder portion is filled, the removal of the oldest data from the remainder portion and so on in a progressive manner over time.

3. A transmitted data receiver according to claim 2 characterised in that the memory operates as a circular or loop configured memory so that when capacity of the memory remainder portion is reached the same is successively overwritten as new packets of data are routed to and through the memory to replace the oldest data in the memory and so on.
4. A transmitted data receiver according to claim 1 characterised in that the capacity of the memory is sufficient to allow the storage of quantities of data to facilitate the storage of a displayed video packet or series of packets for a predetermined period of time following display to allow the same to be re-played or for the same to be used in slow motion.
5. A transmitted data receiver according to claim 4 characterised in that the length of the video display which can selectively be viewed after initial display is directly proportional to the size of the moving circular buffer memory provided.
6. A transmitted data receiver according to claim 1 characterised in that the display interrupt at the video driver layer is manipulated in respect of the contents of the memory so that the selected data can be displayed at selected variable speeds of display.
7. A transmitted data receiver according to claim 1 characterised in that there is provided a facility to select to save a specified packet of data in a memory and to add further packets of data to said memory selectively.
8. A transmitted data receiver according to claim 7 characterised in that the saved packets of data can be re-displayed individually or as a sequence of packets of data.
9. A receiver according to claim 1 characterised in that the first memory portion acts as a buffer.
10. A receiver according to claim 9 characterised in that the first portion is defined as the portion of the memory which lies between the producer and consumer pointers.
11. A receiver according to claim 10 characterised in that the producer and consumer pointers move around the circular configured buffer over time, and as they do so overwrite the oldest data held in the

memory at that time.

12. A method of receiving and displaying transmitted data, said method comprising the steps of:

encoding a stream of data into digital encoded data;  
transmitting the encoded data for reception by at least one data receiver;  
decoding the received data into a form for the generation of a video display on a display screen connected to the data receiver and characterised in that the data receiver includes or is connected to a memory in which the received data can be stored for selected retrieval within a time period after the initial display to create a video display, and wherein said memory includes a first portion into which the most recently received data is placed, and a remainder portion into which data from the first portion passes.

13. A method according to claim 12 characterised in that the received data is simultaneously decoded to create the initial video display and saved in the first portion in the memory for selective use in generating a display thereafter.

14. A method according to claim 12 characterised in that the memory contains packets of received data with the oldest packets of data overwritten by the most recently received packets of data over time.

15. A method according to claim 12 characterised in that packets of data are selectively stored in a memory to generate a series of packets of data for selected display.

16. A method according to claim 12 characterised in that the speed of the display from the memory can be selected by the user.

17. A method according to claim 16 characterised in that the speed of display is selected to provide a slow motion display of the data.

18. A receiver for the reception of transmitted data and generation of a display of the video data on a display screen connected therewith, said receiver including a physical memory in which video data is stored and from which, for a time period following display, said video data can be selectively retrieved to create a video display on the display screen and characterised in that said memory includes a first portion defined as the portion of the memory defined between the producer and consumer pointers which receives the most recently transmitted data received by the receiver at each instant, and a remainder portion of

the memory and in which is stored, in sequence, packets of data which have previously been received and displayed and which are transferred from said first portion as producer and consumer pointers move around the circular memory configuration. 5

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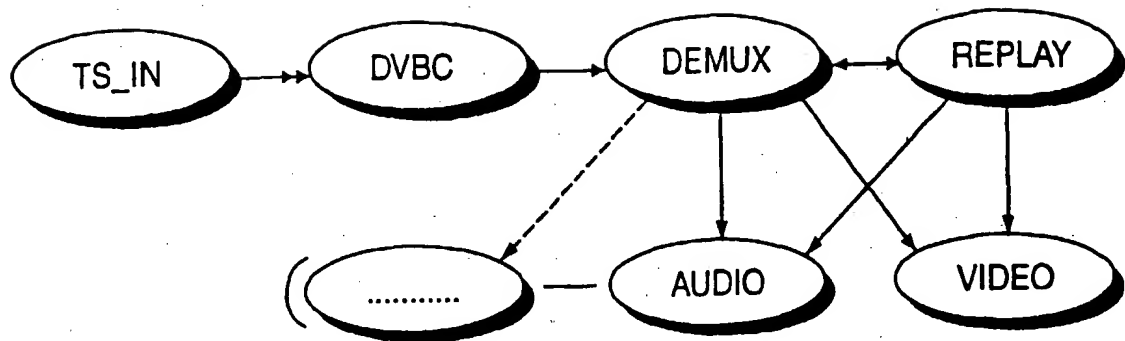


FIG 1

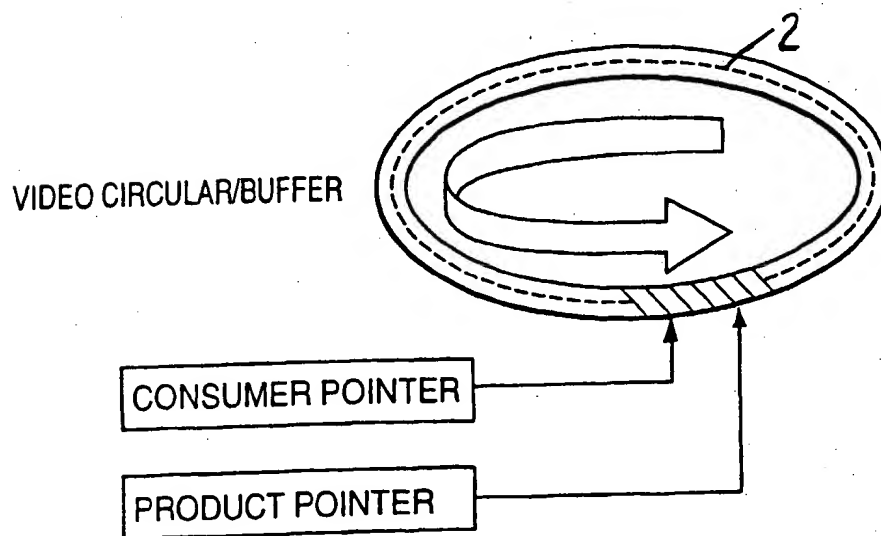
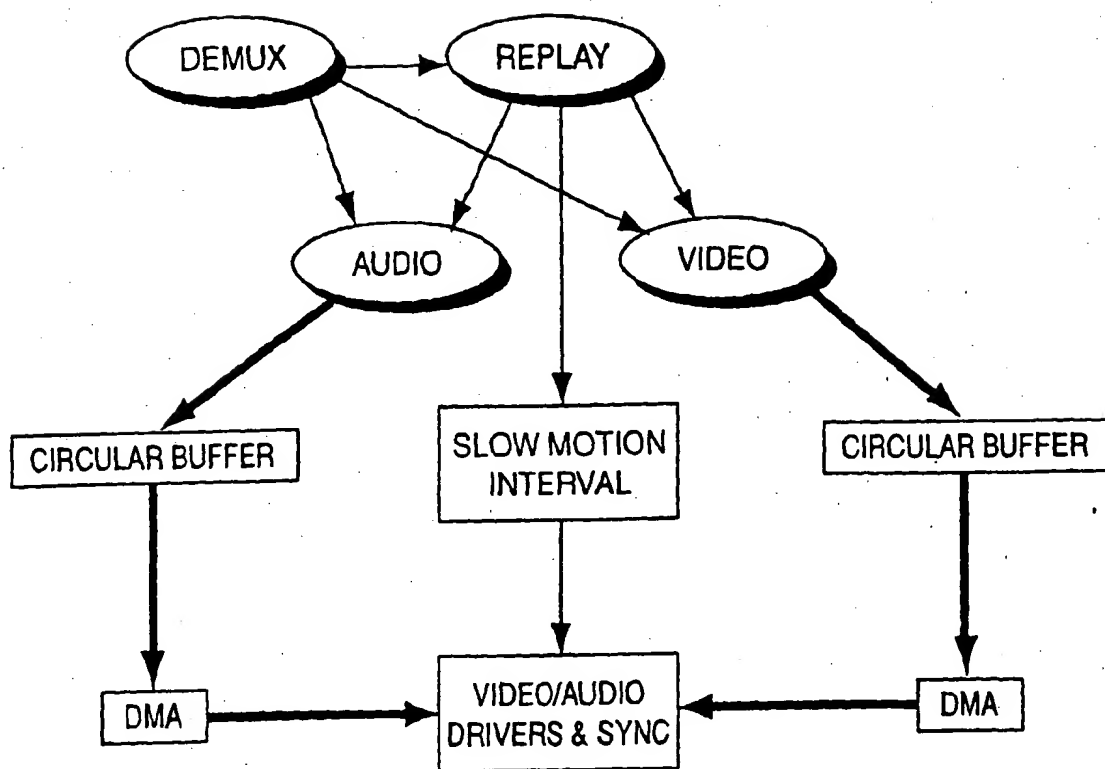
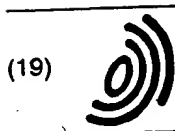


FIG 2

FIG 3





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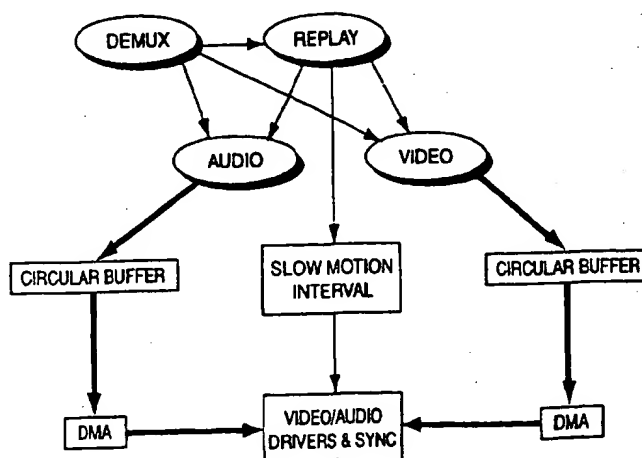


FIG 3

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# EUROPEAN SEARCH REPORT

Application Number  
EP 99 12 4913

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	US 5 371 551 A (LOGAN JAMES ET AL) 6 December 1994 (1994-12-06) * the whole document *	1-18	H04N5/00
X	US 5 774 186 A (MILLMAN STEVEN EDWARD ET AL) 30 June 1998 (1998-06-30) * the whole document *	1-18	
X	EP 0 713 334 A (MATSUSHITA ELECTRIC IND CO LTD) 22 May 1996 (1996-05-22) * column 1, line 1 - column 6, line 37 * * abstract; figures 1,2 *	1-18	
A	US 4 698 664 A (NICHOLS WILLIAM E ET AL) 6 October 1987 (1987-10-06) * the whole document *	1-18	
A	US 5 838 876 A (IWAMURA RYUICHI) 17 November 1998 (1998-11-17) * column 5, line 29 - column 6, line 16 * * figures 4,5 *	1-18	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			H04N
Place of search	Date of completion of the search	Examiner	
THE HAGUE	18 December 2001	La, V	
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# ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

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Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5371551	A	06-12-1994	US RE36801 E	01-08-2000
US 5774186	A	30-06-1998	JP 9233415 A	05-09-1997
			KR 196892 B1	15-06-1999
EP 0713334	A	22-05-1996	CN 1151081 A	04-06-1997
			EP 0713334 A2	22-05-1996
			JP 8237592 A	13-09-1996
			KR 222013 B1	01-10-1999
			US 5822493 A	13-10-1998
US 4698664	A	06-10-1987	NONE	
US 5838876	A	17-11-1998	NONE	

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